

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Doan et al.

**Serial No.:** 10/644,685

**Filed:** August 20, 2003

**For:** CONTROLLABLE OVONIC PHASE-  
CHANGE SEMICONDUCTOR MEMORY  
DEVICE (as amended)

**Confirmation No.:** 6457

**Examiner:** O. Nadav

**Group Art Unit:** 2811

**Attorney Docket No.:** 2269-3414.7US  
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**Notice of Allowance Mailed:**

March 13, 2007

**VIA ELECTRONIC FILING**  
June 13, 2007

**FEE ADDRESSEE FOR RECEIPT OF PTO NOTICES  
RELATING TO MAINTENANCE FEES**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

1. This letter is to specify that the FEE ADDRESSEE for this patent is:

MICRON TECHNOLOGY, INC.  
Mail Stop 525  
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2. The Customer Number for the Fee Addressee is **26809**.
3. Any prior FEE ADDRESSEE for this patent is hereby revoked.
4. It is certified that the person whose signature appears below has the authority to change the FEE ADDRESSEE for this patent.

Respectfully submitted,



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KWP/sfc:lmh

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